

General Description

- NMSD200B01 is best suited for switching voltage regulator and power management applications. It improves efficiency and reliability of DC-DC controllers used in Voltage Regulator Modules (VRM) and can support continuous maximum current of 200mA. It features an ESD protected discrete N-MOSFET with low on-resistance and a discrete Schottky diode with low forward drop. It reduces component count, consumes less space and minimizes parasitic losses. The component devices can be used as a part of a circuit or as a stand alone discrete device.

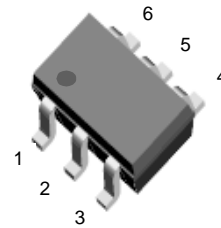


Fig 1. SOT-363

Features

- N-MOSFET with ESD Gate Protection
- N-MOSFET with Low On-Resistance ($R_{DS(ON)}$)
- Low V_f Schottky Diode
- Low Static, Switching and Conduction Losses
- Good Dynamic Performance
- Surface Mount Package Suited for Automated Assembly
- Lead Free By Design/RoHS Compliant (Note 1)**
- "Green" Device (Note 2)**

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. "Green Molding" Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 7
- Ordering Information: See Last Page
- Weight: 0.006 grams (approximate)

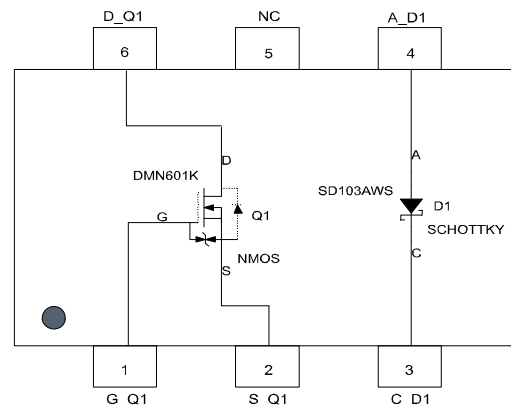


Fig 2. Schematic and Pin Configuration

Sub-Components	Reference	Device Type	Figure
DMN601K_DIE (ESD Protected)	Q1	N-MOSFET	2
SD103AWS_DIE	D1	Schottky Diode	2

Maximum Ratings, Total Device @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P_d	200	mW
Power Derating Factor above 25°C	P_{der}	1.6	mW/ $^\circ\text{C}$
Output Current	I_{out}	200	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Junction Operation and Storage Temperature Range	T_j, T_{stg}	-55 to +150	$^\circ\text{C}$
Thermal Resistance, Junction to Ambient Air (Note 3) (Equivalent to one heated junction of N-MOSFET)	$R_{\theta JA}$	625	$^\circ\text{C/W}$

- Notes:
- No purposefully added lead.
 - Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 - Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.

Maximum Ratings: @T_A = 25°C unless otherwise specified

Sub-Component Device: ESD Protected N-Channel MOSFET (Q1)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	60	V
Drain Gate Voltage (R _{GS} <+ 1MΩ)	V _{DGR}	60	V
Gate Source Voltage	V _{GSS}	+/-20	V
Continuous Pulsed (tp<50 uS)		+/-40	
Drain Current (Page 1: Note 3)	I _D	200	mA
Continuous (V _{GS} =10V)		800	
Pulsed (tp<10uS, Duty Cycle<1%)	I _S	200	mA

Sub-Component Device: Schottky Diode (D1) @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	28	V
Forward Continuous Current (Page 1: Note 3)	I _{FM}	350	mA
Non-Repetitive Peak Forward Surge Current @ t<1.0 s	I _{FSM}	1.5	A

Electrical Characteristics:

ESD Protected N-Channel MOSFET (Q1) @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage, BV _{DSS}	V _{BR(DSS)}	60	—	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current (Drain Leakage Current)	I _{DSS}	—	—	1	μA	V _{GS} = 0V, V _{DS} = 60V
Gate Body Leakage Current, Forward	I _{GSSF}	—	—	10	μA	V _{GS} = 20V, V _{DS} = 0V
Gate Body Leakage Current, Reverse	I _{GSSR}	—	—	-10	μA	V _{GS} = -20 V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Source Threshold Voltage (Control Supply Voltage)	V _{GS(th)}	1	1.6	2.5	V	V _{DS} = V _{GS} =10V, I _D = 0.25mA
		1.1	1.8	3	V	V _{DS} = V _{GS} = 10V, I _D = 1mA
Static Drain-Source On-State Voltage	V _{DS(on)}	—	0.09	1.5	V	V _{GS} = 5V, I _D = 50mA
		—	0.62	1.25	V	V _{GS} = 10V, I _D = 500mA
On-State Drain Current	I _{D(on)}	500	—	—	mA	V _{GS} = 10V, V _{DS} >=2*V _{DS(ON)}
Static Drain-Source On Resistance	R _{DS (on)}	—	1.6	3	Ω	V _{GS} = 5V, I _D = 50mA
		—	1.25	2		V _{GS} = 10V, I _D = 500mA
Forward Transconductance	g _{FS}	80	260	—	mS	V _{DS} >=2*V _{DS(ON)} , I _D =200mA
Dynamic Characteristics						
Input Capacitance	C _{iss}	—	—	50	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C _{rss}	—	—	5	pF	
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	—	—	20	ns	—
Turn-Off Delay Time	t _{d(off)}	—	—	40	ns	—
Drain-Source (Body) Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward On-Voltage	V _{SD}	—	0.88	1.5	V	V _{GS} = 0V, I _S = 300 mA*
Maximum Continuous Drain-Source Diode Forward Current (Reverse Drain Current)	I _S	—	—	300	mA	—
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}	—	—	800	mA	—

Electrical Characteristics: Schottky Barrier Diode (D1) @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V _{(BR)R}	40	—	—	V	I _R = 10μA
Forward Voltage Drop (Note 4)	V _{FM}	—	—	0.37	V	I _F =20mA
		—	—	0.6		I _F =200mA
Peak Reverse Current (Note 4)	I _{RM}	—	—	5	μA	V _R = 30V
Total Capacitance	C _T	—	28	—	pF	V _R = 0V, f = 1.0 MHz
Reverse Recovery Time	t _{rr}	—	10	—	ns	I _F =I _R = 200 mA, I _{rr} = 0.1xI _R , R _L = 100 Ω

Notes: 4. Short duration pulse test used to minimize self-heating effect.

Typical Characteristics

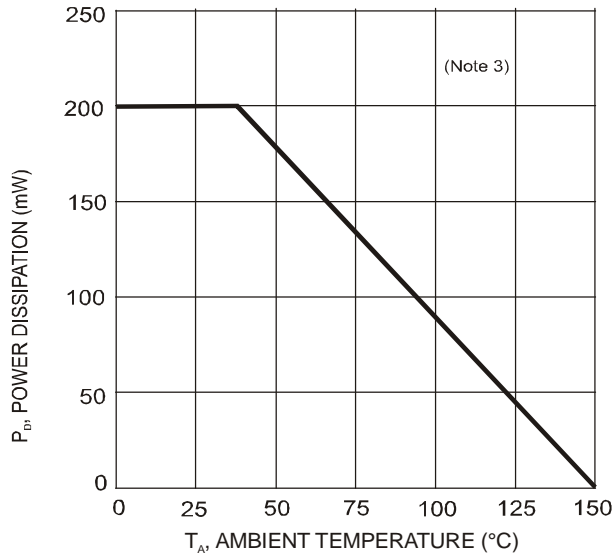


Fig. 3, Max Power Dissipation vs. Ambient Temperature

Typical N-Channel MOSFET-Q1 (ESD Protected) Characteristics

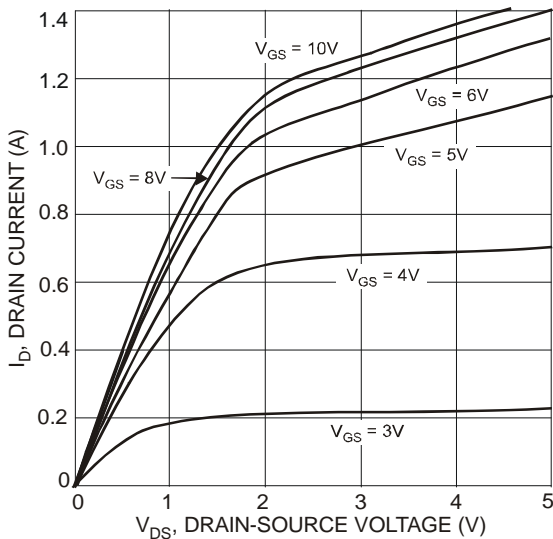


Fig. 4 Output Characteristics

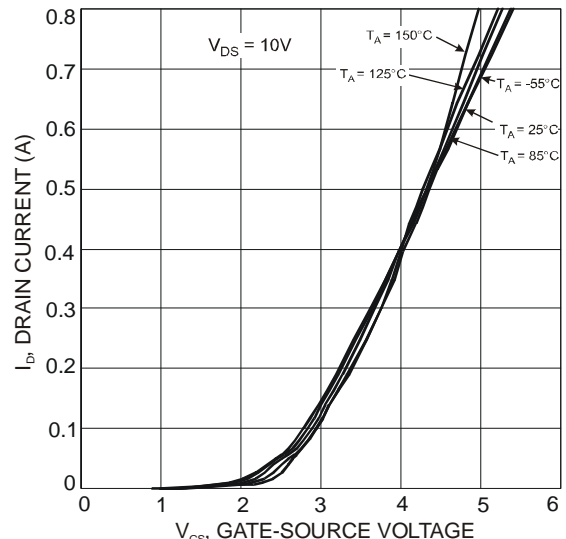


Fig. 5 Transfer Characteristics

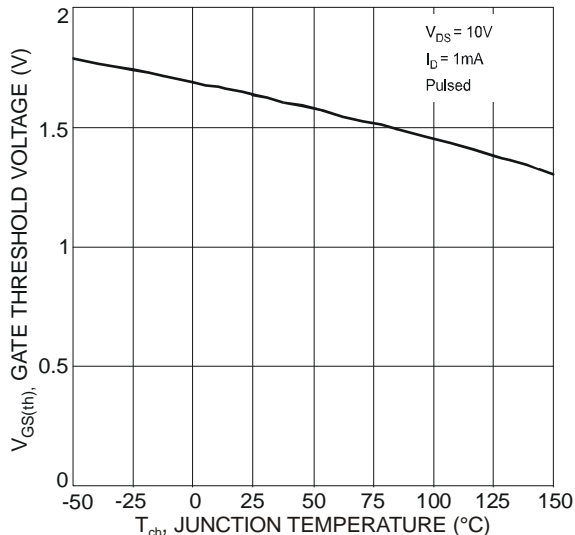


Fig. 6 Gate Threshold Voltage vs. Junction Temperature

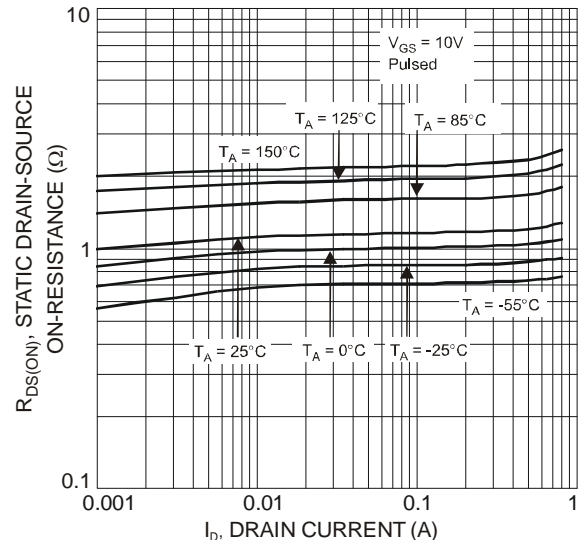


Fig. 7 Static Drain-Source On-Resistance vs. Drain Current

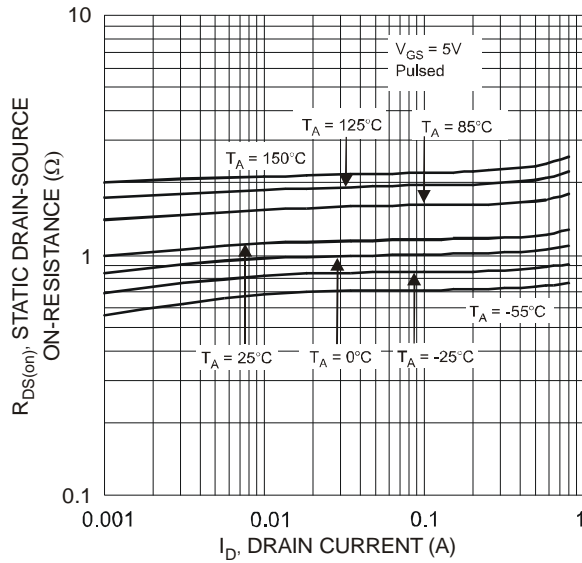


Fig. 8 Static Drain-Source On-Resistance vs. Drain Current

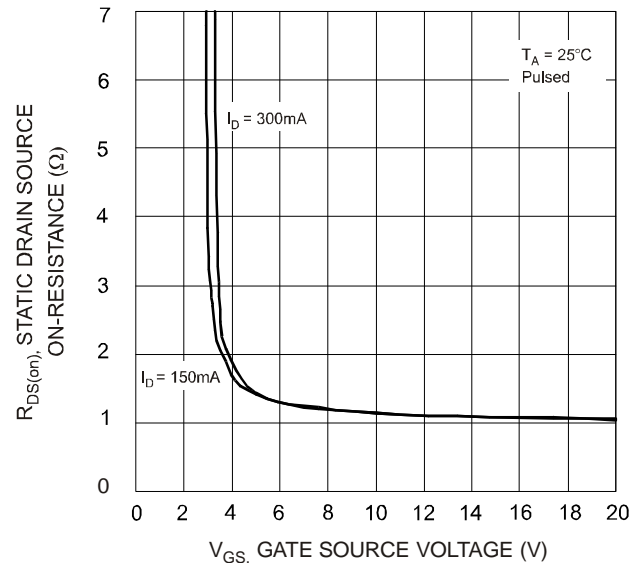


Fig. 9 Static Drain-Source On-Resistance vs. Gate-Source Voltage

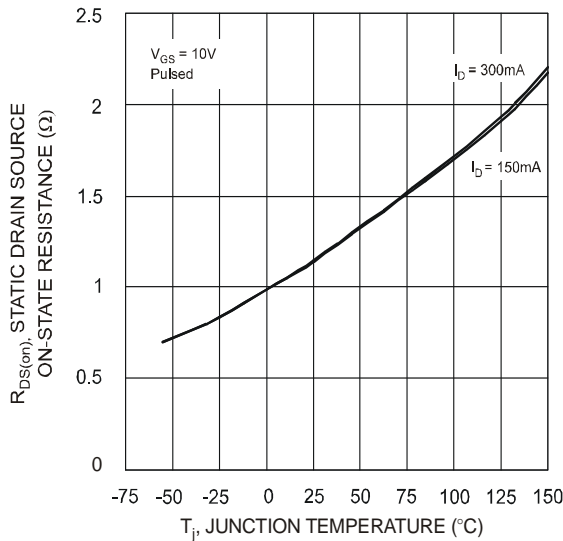


Fig. 10 Static Drain-Source On-State Resistance vs. Junction Temperature

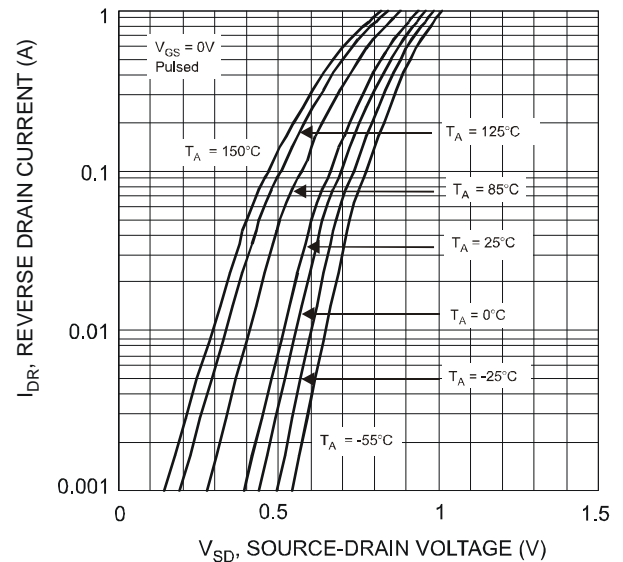


Fig. 11 Reverse Drain Current vs. Source-Drain Voltage

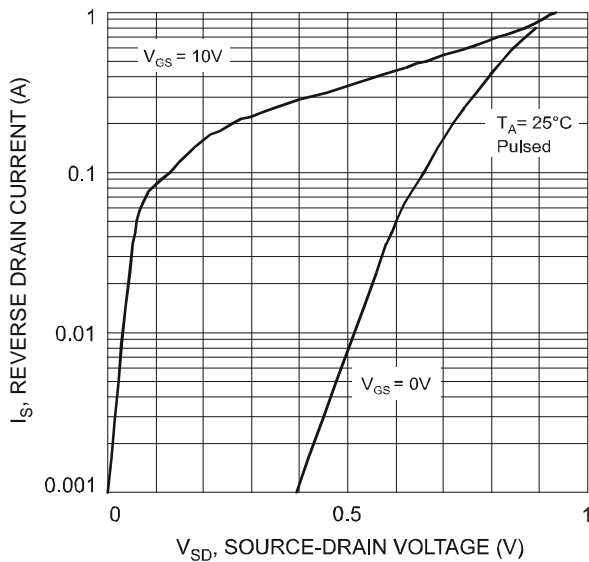


Fig. 12 Reverse Drain Current vs. Body Diode Forward Voltage

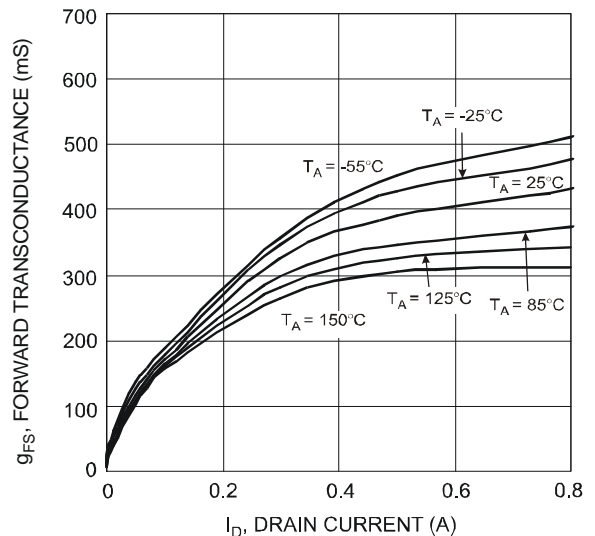
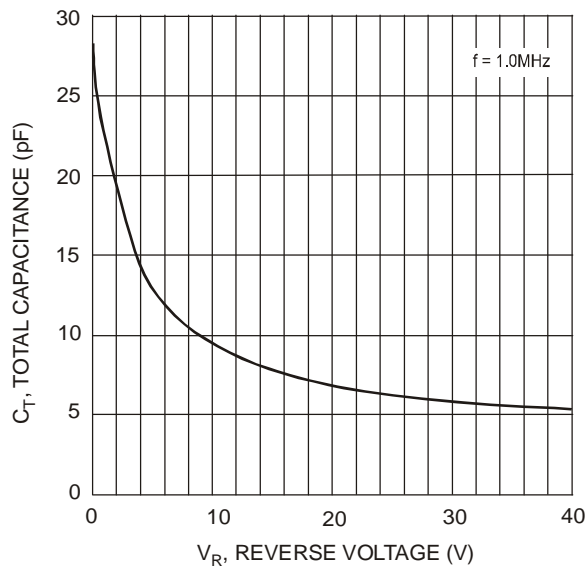
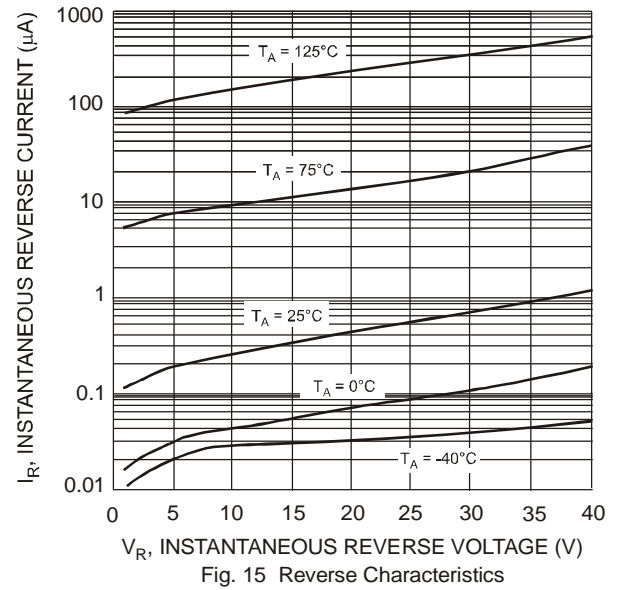
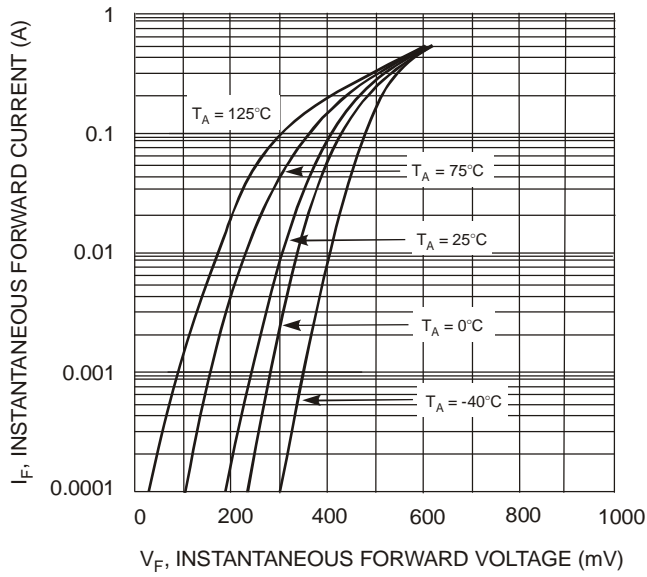


Fig. 13 Forward Transconductance vs. Drain Current ($V_{DS} > I_D \cdot R_{DS(ON)}$)

Schottky Barrier Diode – D1 Characteristics



Application Details

ESD Protected N-MOSFET (DMN601K) and Schottky Barrier Diode (SD103AWS) integrated as one in NMSD200B01 can be used as a discrete entity for general applications or part of circuits to function as a low side switch in a Synchronous Rectifier. The N-MOSFET is selected based on the input voltage range as the maximum duty cycles can be greater than 45%. Schottky diode is selected based on instantaneous V_f (less than 0.75 V) at maximum operation current. The Schottky diode dissipates very little power because it is on for only a small portion of the switching cycle. Normally it shows much lower leakage current and smaller on-resistance ($R_{DS(ON)}$) even compared to its monolithic counterpart. This device is designed to improve efficiency and reliability of synchronous buck converters used in voltage regulator modules (VRM). The lower V_f of the Schottky diode leads to lower static loss. Every time the high side MOSFET is turned on in the buck converter, the low side Schottky diode is forced to recover the stored charge and there will be lower loss due to the lower Reverse Recovery charge of the Schottky diode.

It is designed to replace a discrete N-MOSFET and a Schottky diode in two separate packages into one small package as shown in Fig. 17. The Schottky diode parallel to the MOSFET body diode is faster and has lower voltage drop compared to the integrated body diode. Overall this device consumes less board space and also helps to minimize conduction or switching losses due to parasitic inductances (e.g. PCB traces) in power supply applications. (Please see Fig. 18 for one example of typical application circuit used in conjunction with DC-DC converter as a part of power management system and Fig. 19 for low side DC load control.)

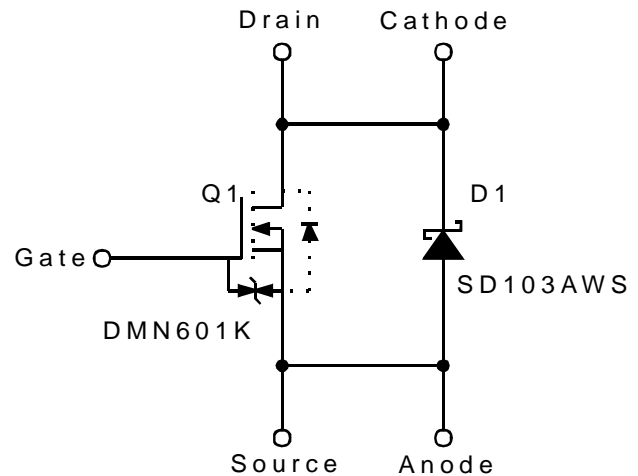


Fig. 17 Example Circuit Diagram

Typical Application Circuits

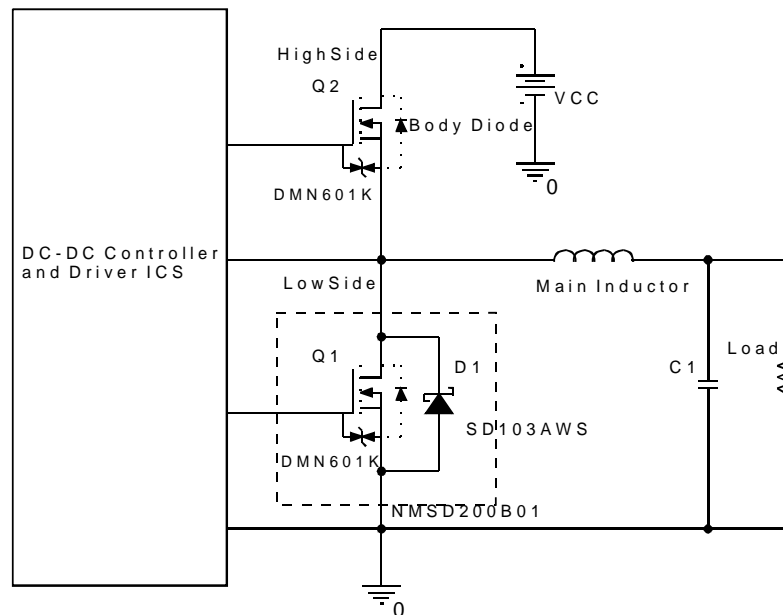


Fig. 18 Synchronous Buck Converter with Integrated Schottky Diode

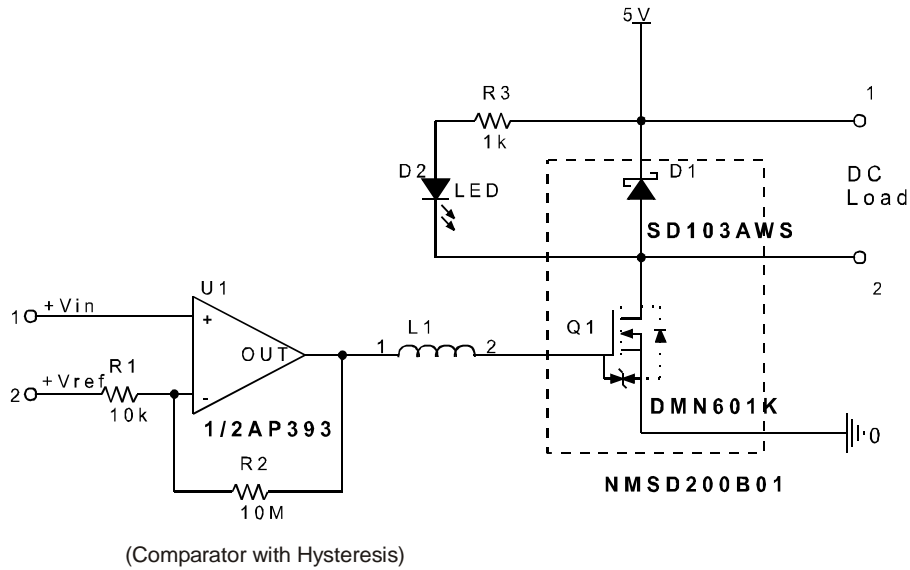


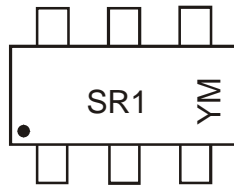
Fig. 19 Low Side DC Load Control

Ordering Information (Note 5)

Device	Marking Code	Packaging	Shipping
NMSD200B01-7	SR1	SOT-363	3000/Tape & Reel

Notes: 5. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



SR1 = Product Type Marking Code,
YM = Date Code Marking
Y = Year, e.g., T = 2006
M = Month, e.g., 9 = September

Fig. 20

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Mechanical Details

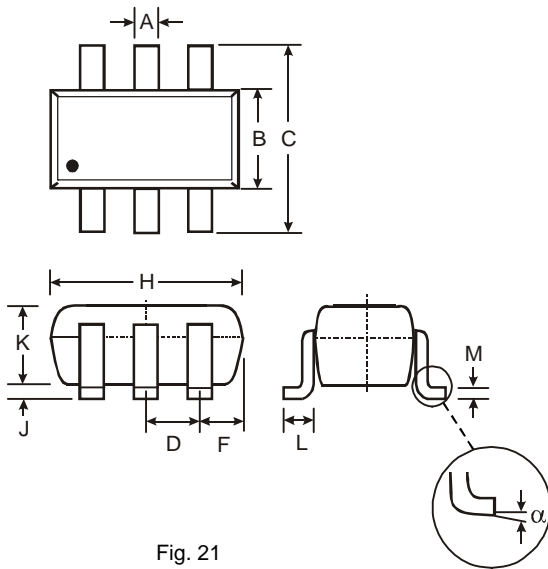


Fig. 21

SOT-363		
Dim	Min	Max
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
F	0.30	0.40
H	1.80	2.20
J	—	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.25
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout: (Based on IPC-SM-782)

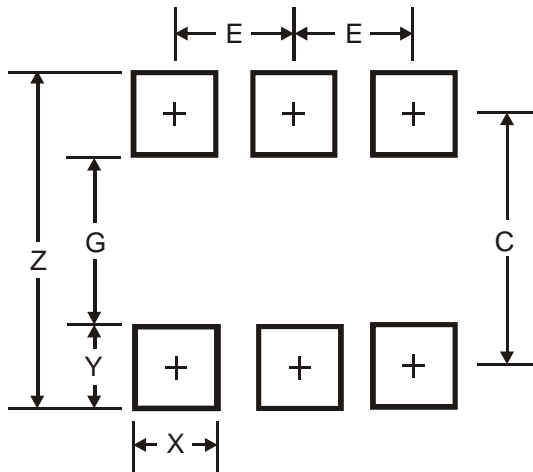


Fig. 22

Figure 22 Dimensions	SOT-363*
Z	2.5
G	1.3
X	0.42
Y	0.6
C	1.9
E	0.65
*Typical dimensions in mm	

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